Notice of References Cited		Application/Control No. 09/853,226		Applicant(s)/Patent Under Reexamination STRONGIN, GEOFFREY S.	
		Examiner Kevin P. Rizzuto		Art Unit 2183	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,826,007	10-1998	Sakaki et al.	714/42
	B	US-6,026,016	02-2000	Gafken, Andrew H.	365/185.04
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Kime, Charles R. and Mano, M. Morris. Logic and Computer Design Fundamentals, 2nd ed. 2000. Pages 185 and 186.
	V	Short, Kenneth L. Embedded Microprocessor Systems Design, An INtroduction Using the Intel 80C188EB. Prentice-Hall, Inc. 1998. Page 35.
	W	The Authoritative Dictionary of IEEE Standards Terms, 7th ed. Standards Information Network, IEEE Press. 2000.
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.